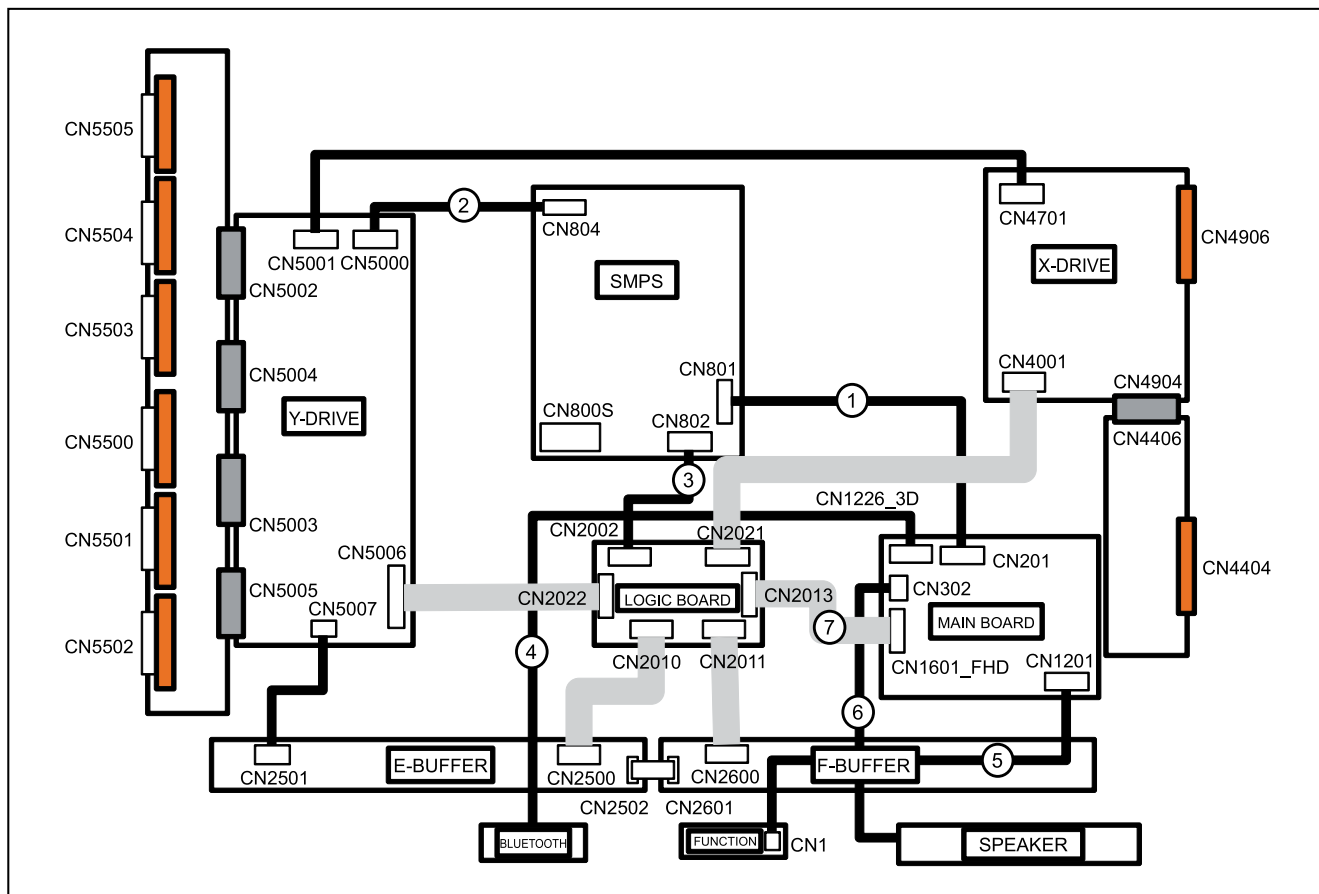


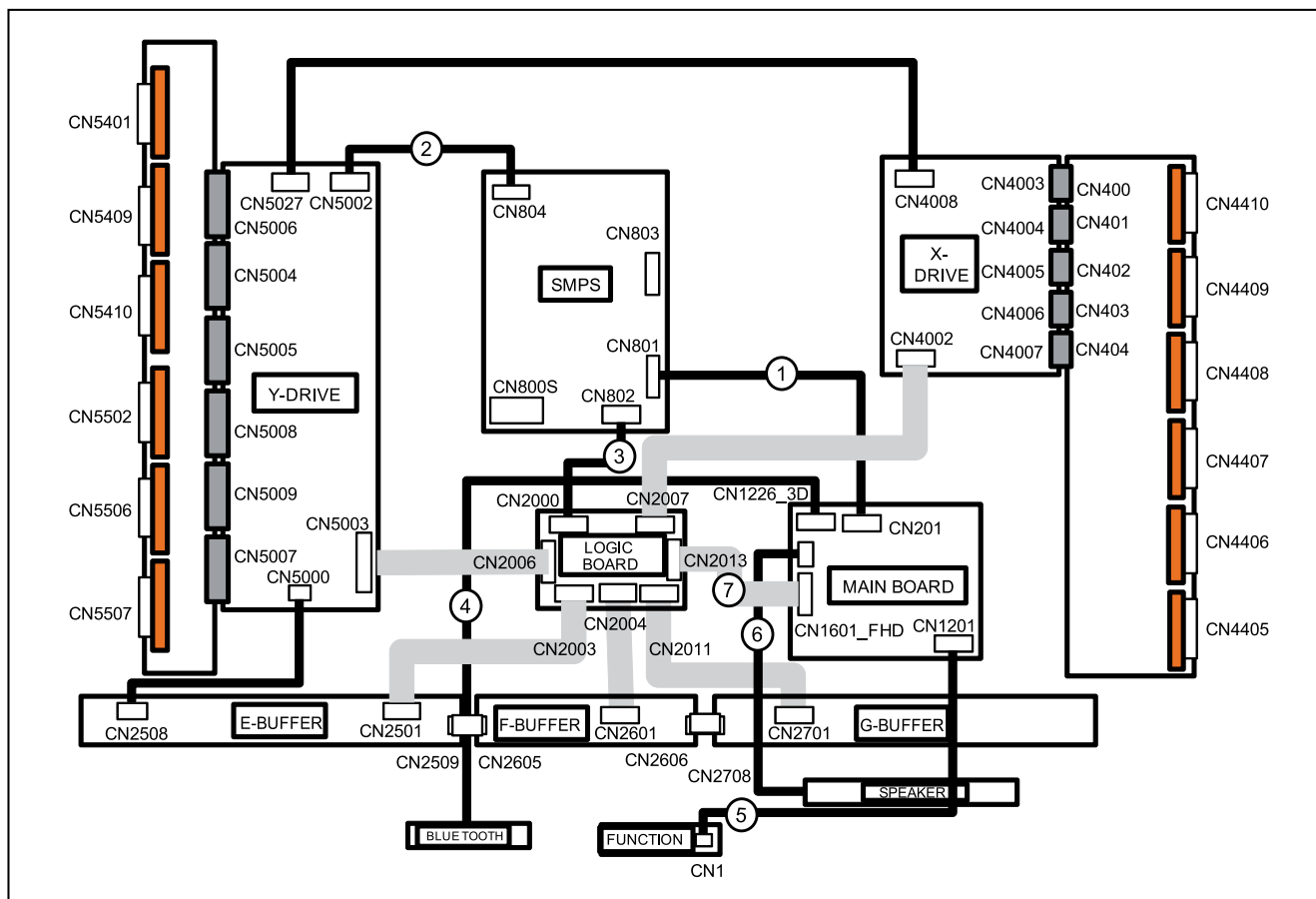
5. Wiring Diagram

5.1. Overall Wiring

■ 51" FHD Overall Wiring



■ 59" / 64" FHD Overall Wiring



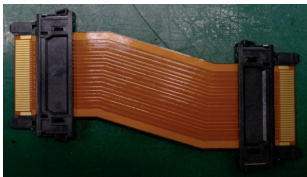
**NOTE**

The code number of cable (Lead-connector) can be changed, see “Exploded Views and Parts List”.

■ 51" Cable (Lead-connector)

Use	(1) POWER 12 Pin	(7) LVDS FFC 82 Pin
Code	BN39-01285L	BN96-18130E
Photo		

■ 59" / 64" Cable (Lead-connector)

Use	(1) POWER 12 Pin	(4) Bluetooth Cable 8 Pin	(7) LVDS FFC 82 Pin
Code	BN39-01285D	59" – BN39-01447B 64" – BN39-01447D	BN96-18130F
Photo			

5.1.1. Pin Connection

(1) CN801 (SMPS) ↔ CN201 (Main Board)			
Pin No. (SMPS)	Signal (SMPS)	Pin No. (Main Board)	Signal (Main Board)
1	PS-ON	1	SW_POWER
2	STBY	2	A5V_PW
3	GND	3	DGND
4	D15V	4	B15VS_PW
5	GND	5	DGND
6	GND	6	DGND
7	D5.3V	7	B5V_PW
8	D5.3V	8	B5V_PW
9	GND	9	DGND
10	D15V	10	B15V_PW
11	D15V	11	B15V_PW
12	D5.3V	12	B5V_PW

(2) 51" only : CN804 (SMPS) ↔ CN5000 (Y Board) 59"/64" only : CN804 (SMPS) ↔ CN5002 (Y Board)			
Pin No. (SMPS)	Signal (SMPS)	Pin No. (Y Board)	Signal (Y Board)
1	208V	1	Vs
2	208V	2	Vs
3	GND	3	GND
4	D15V	4	Vg(15V)
5	GND	5	GND
6	VA	6	Va

(3) 51" only : CN802 (SMPS) ↔ CN2002 (Logic Board) 59"/64" only : CN802 (SMPS) ↔ CN2000 (Logic Board)			
Pin No. (SMPS)	Signal (SMPS)	Pin No. (Logic Board)	Signal (Logic Board)
1	D5.3V	1	5.3V
2	D5.3V	2	5.3V
3	GND	3	GND
4	VS-SIGNAL	4	GND
5	PS-ON	5	PS_ON
6	VS-ON	6	VS_ON

(4) CN1226D_3D (Main Board) ↔ BLUETOOTH			
Pin No. (Main Board)	Signal (Main Board)	Pin No. (BLUETOOTH)	Signal (BLUETOOTH)
1	FRAME_SYNC_IN	1	Reset_Module
2	FRAME_SYNC_OUT	2	VCC 5V
3	DGND	3	USB D-(BT)
4	USB_BT_DP	4	USB D+(BT)
5	USB_BT_DM	5	GND
6	A5V_PM	6	3D Sync Out
7	FUNC_INTR	7	3D Sync In
8	POWER_DET	8	

(5) CN1201 (Main Board) ↔ CN1 (FUNCTION)			
Pin No. (Main Board)	Signal (Main Board)	Pin No. (FUNCTION)	Signal (FUNCTION)
1	IR	1	IR
2	FUNC_GND	2	GND
3	FUNC_A3.3V	3	A3.3V
4	MSCT	4	SCL
5	MSDA	5	SDA
6	FUNC_INTR	6	INT
7	LED_STR	7	LED

(6) CN302 (Main Board) ↔ SPEAKER	
Pin No. (Main Board)	Signal (Main Board)
1	R+_OUT
2	R-_OUT
3	L+_OUT
4	L-_OUT

(7) Logic Board Pin CN2013 (Logic Board) ↔ CN1601_FHD (Main Board)							
Pin No.	Signal	Pin No.	Signal	Pin No.	Signal	Pin No.	Signal
1	I2C_READY	22	Ch1[4]-	43	GND	64	CH4[1]-
2	GND	23	Ch1[4]+	44	SDA	65	Ch4[0]+
3	3D_SYNC	24	GND	45	GND	66	CH4[0]-
4	GND	25	Ch3[0]-	46	N/C	67	GND
5	GND	26	Ch3[0]+	47	GND	68	Ch2[4]+
6	N/C	27	Ch3[1]-	48	UART Rx	69	Ch2[4]-
7	GND	28	Ch3[1]+	49	GND	70	Ch2[3]+
8	GND	29	Ch3[2]-	50	UART Tx	71	Ch2[3]-
9	GND	30	Ch3[2]+	51	GND	72	GND
10	Ch1[0]-	31	GND	52	GND	73	Ch2CLK+
11	Ch1[0]+	32	Ch3CLK-	53	Ch4[4]+	74	Ch2CLK-
12	Ch1[1]-	33	Ch3CLK+	54	CH4[4]-	75	GND
13	Ch1[1]+	34	GND	55	Ch4[3]+	76	Ch2[2]+
14	Ch1[2]-	35	Ch3[3]-	56	CH4[3]-	77	Ch2[2]-
15	Ch1[2]+	36	Ch3[3]+	57	GND	78	Ch2[1]+
16	GND	37	Ch3[4]-	58	Ch4CLK+	79	Ch2[1]-
17	Ch1CLK-	38	Ch3[4]+	59	Ch4CLK-	80	Ch2[0]+
18	Ch1CLK+	39	GND	60	GND	81	Ch2[0]-
19	GND	40	SCL	61	Ch4[2]+	82	GND
20	Ch1[3]-	41	GND	62	CH4[2]-		
21	Ch1[3]+	42	N/C	63	Ch4[1]+		

(7) Main Board Pin CN2013 (Logic Board) ↔ CN1601_FHD (Main Board)							
Pin No.	Signal	Pin No.	Signal	Pin No.	Signal	Pin No.	Signal
1	GND	22	Ch3[2]+	43	GND	64	Ch2[3]+
2	UART Tx	23	Ch3[2]-	44	GND	65	Ch2[4]-
3	GND	24	Ch3[1]+	45	GND	66	Ch2[4]+
4	UART Rx	25	Ch3[1]-	46	N/C	67	GND
5	GND	26	Ch3[0]+	47	GND	68	Ch4[0]-
6	N/C	27	Ch3[0]-	48	GND	69	Ch4[0]+
7	GND	28	GND	49	3D_SYNC	70	Ch4[1]-
8	SDA	29	Ch1[4]+	50	GND	71	Ch4[1]+
9	GND	30	Ch1[4]-	51	N/C	72	Ch4[2]-
10	N/C	31	Ch1[3]+	52	GND	73	Ch4[2]+
11	GND	32	Ch1[3]-	53	Ch2[0]-	74	GND
12	SCL	33	GND	54	Ch2[0]+	75	Ch4CLK-
13	GND	34	Ch1CLK+	55	Ch2[1]-	76	Ch4CLK+
14	Ch3[4]+	35	Ch1CLK-	56	Ch2[1]+	77	GND
15	Ch3[4]-	36	GND	57	Ch2[2]-	78	Ch4[3]-
16	Ch3[3]+	37	Ch1[2]+	58	Ch2[2]+	79	Ch4[3]+
17	Ch3[3]-	38	Ch1[2]-	59	GND	80	Ch4[4]-
18	GND	39	Ch1[1]+	60	Ch2CLK-	81	Ch4[4]+
19	Ch3CLK+	40	Ch1[1]-	61	Ch2CLK+	82	GND
20	Ch3CLK-	41	Ch1[0]+	62	GND		
21	GND	42	Ch1[0]-	63	Ch2[3]-		